

IN THE CLAIMS:

1. (currently amended) ~~A noise reduction filter array, An array type noise reduction filter, comprising: a plurality of noise reduction filters horizontally arranged within a single chip, each comprising: ;~~

~~two an inductance portions each comprising comprised of first and second coils which are electrically connected together, one of the first and second coils being put on top of the other; approximately vertically connected in the chip;~~

~~a ground portion arranged over or under the inductance portion; ; and~~

~~a capacitance portion arranged over or under the ground portion;~~

~~wherein a the first coils of said inductance portions are wound in the same direction, and the second coils of said any inductance portions are is constructed to be wound in a opposite directions opposite to a second coil of another adjacent inductance portion.~~

2. (currently amended) ~~The noise reduction filter array The array type noise reduction filter~~ according to claim 1, wherein the first and second coils have approximately the same inductance value.

3. (currently amended) ~~The noise reduction filter array array type noise reduction filter~~ according to claim 1, wherein the ground portion is ~~comprised of~~ comprises a first ground portion arranged over the inductance portion and a second ground portion arranged under the inductance portion.

4. (currently amended) ~~The noise reduction filter array array type noise reduction filter~~ according to claim 3, wherein the capacitance portion is ~~comprised of~~ comprises a first capacitance portion arranged under the first ground portion and a second capacitance portion arranged over the second the ground portion.

5. (currently amended) ~~The noise reduction filter array array type noise reduction filter~~ according to claim 1, wherein the ground portion is ~~only arranged either over or under the inductance portion at only one position of the upper and lower portions of the chip,~~ and the capacitance portion is ~~comprised of~~ comprises a first capacitance portion arranged over the ground portion and a second capacitance portion arranged under the ground

portion.

6. (currently amended) The ~~noise reduction filter array array type noise reduction filter~~ according to claim 1, wherein the ground portion is a common electrode formed as a single layer ~~to be~~ and shared between ~~said inductance portions~~ a plurality of noise reduction filters.

7. (currently amended) The ~~noise reduction filter array array type noise reduction filter~~ according to claim 1, ~~further comprising, for each of said inductance portions,~~ wherein ~~a plurality of noise reduction filters each further comprise~~ isolation means formed ~~positioned~~ between the first and second coils ~~for to~~ blocking electromagnetic influence ~~therebetween~~ between the first and second coils.

8. (currently amended) The ~~noise reduction filter array array type noise reduction filter~~ according to claim 7, wherein the isolation means ~~is formed of~~ comprise a conductor layer having a via hole for ~~connecting~~ electrical connection between the first and second coils ~~to each other~~.

9. (currently amended) ~~A noise reduction filter array, comprising~~ An array type noise reduction filter having ~~a plurality of filters horizontally arranged side by side within a single chip, each filter a single chip shape,~~ comprising:

an ~~plurality of inductance portions each comprised of~~ comprising first and second coils ~~which are serially electrically connected together, the first coil being vertically arranged on top of the second coil approximately vertically connected within a chip, and arranged horizontally to each other;~~

a ground portion ~~vertically arranged at at least one position over or under the inductance portions according to the arrangement direction of a plurality of inductance portions;~~ and

a ~~plurality of capacitance portions each vertically arranged over or under the ground portion, and in approximately the same direction as that of each of a plurality of inductance portions;~~

~~wherein the inductance portions each include~~ isolation means for blocking electromagnetic influence between the first and second coils of ~~each of the inductance portions;~~ , and ~~a second coil of any inductance portion is constructed to be wound in a~~

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~~direction opposite to a second coil of another adjacent inductance portion~~

wherein, for two adjacent said filters, the first coils of the inductance portions of said adjacent filters are wound in the same direction, and the second coils of the inductance portions of said adjacent filters are wound in opposite directions.

10. (new) The noise reduction filter array according to claim 1, being incorporated into a single chip.

11. (new) The noise reduction filter array according to claim 1, wherein a first mutual inductance induced between the first coils and a second mutual inductance induced between the second coils have opposite signs.

12. (new) The noise reduction filter array according to claim 11, wherein the first mutual inductance is substantially entirely offset by the second mutual inductance.

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13. (new) The noise reduction filter array according to claim 1, wherein the first coils are arranged to be substantially coplanar with each other, and the second coils are arranged to be substantially coplanar with each other.

14. (new) The noise reduction filter array according to claim 1, comprising a plurality of said inductance portions arranged side by side in a row,

wherein, for any two adjacent said inductance portions, the first coils and one of the second coils are wound in a first direction whereas the other second coil is wound in a second direction opposite to the first direction.

15. (new) A noise reduction filter array, comprising at least two inductive elements arranged side by side, each of said inductive elements comprising a first coil and a second coil electrically connected with each other, wherein a first mutual inductance induced between the first coils and a second mutual inductance induced between the second coils have opposite signs.

16. (new) The noise reduction filter array according to claim 15, wherein the first mutual inductance is substantially entirely offset by the second mutual inductance.

17. (new) The noise reduction filter array according to claim 15, further comprising at least two capacitive elements, wherein each of said inductive elements is electrically coupled with one of said capacitive elements to form a filter.

18. (new) The noise reduction filter array according to claim 17, wherein the first coils and one of the second coils are wound in a first direction whereas the other second coil is wound in a second direction opposite to the first direction; all of said first and second coils have approximately the same inductance value; and for each of said inductive elements, the first and the second coils are axially arranged one on top of the other.

19. (new) The noise reduction filter array according to claim 15, wherein said noise reduction filter array is incorporated in a single chip; the first coils and one of the second coils are wound in a first direction whereas the other second coil is wound in a second direction opposite to the first direction; and for each of said inductive elements, the first and the second coils are axially arranged one on top of the other.

20. (new) The noise reduction filter array according to claim 19, wherein all of said first and second coils have approximately the same inductance value.